Refine Search

Search Results -

Terms	Documents
L16 and L17	18

Database:
```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
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Search:
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L18
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Refine Search

Recall Text ⬍   Clear   Interrupt

Search History

DATE: Tuesday, January 20, 2004   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=YES; OP=OR		
L18	l16 and l17	18	L18
L17	(flash with ((internal$3 or local$3) near4 bus))	287	L17
L16	((processor or CPU or host) with (execut$4 or run$4) with (program or instruction or code) and (bus with flash) and ((program or instruction or code) with (power$3 or startup or start$3) with (volatile or RAM or SRAM or DRAM or (random adj2 memory))))	162	L16
L15	((processor or CPU or host) with (execut$4 or run$4) with (program or instruction or code) and (bus with (volatile or RAM or SRAM or DRAM or (random adj2 memory))) and (bus with flash))	810	L15
L14	((processor or CPU or host) with (execut$4 or run$4) with (program or instruction or code) and (volatile or RAM or SRAM or DRAM or (random adj2 memory)) near8 (code or (program adj2 instruction)) and flash near8 (code or (program adj2 instruction)) and (bus with (volatile or RAM or SRAM or DRAM or (random adj2 memory))))	188	L14

h   e b   b cg b   e e ch

L13	((processor or CPU or host) with (execut$4 or run$4) with (program or instruction or code) and (volatile or RAM or SRAM or DRAM or (random adj2 memory)) near8 (code or (program adj2 instruction)) and flash near8 (code or (program adj2 instruction)))	359	L13
L12	((processor or CPU or host) with (execut$4 or run$4) with (program or instruction or code))	56444	L12
L11	((volatile or RAM or SRAM or DRAM or (random adj2 memory)) near8 (code or (program adj2 instruction)))	12251	L11
L10	(flash near8 (code or (program adj2 instruction)))	2299	L10

DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L9	L8 and l7 and l6	0	L9
L8	(volatile or RAM or SRAM or DRAM or (random adj2 memory)) with (execut$4 or run$4) near6 (program or instruction or code)	3106	L8
L7	(processor or CPU or host) with (execut$4 or run$4) with (program or instruction or code)	21195	L7
L6	flash with ((internal$3 or local$3) near4 bus)	32	L6

DB=USPT; PLUR=YES; OP=OR

L5	L4 and l3 and l2	69	L5
L4	(volatile or RAM or SRAM or DRAM or (random adj2 memory)) with (execut$4 or run$4) near6 (program or instruction or code)	14930	L4
L3	(processor or CPU or host) with (execut$4 or run$4) with (program or instruction or code)	56444	L3
L2	flash with ((internal$3 or local$3) near4 bus)	287	L2
L1	((processor or CPU or host) with (execut$4 or run$4) with (program or instruction or code) and (bus with flash) and ((program or instruction or code) with (power$3 or startup or start$3) with (volatile or RAM or SRAM or DRAM or (random adj2 memory))))	162	L1

END OF SEARCH HISTORY

h   e b   b cg b   e e ch